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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/939,199	08/24/2001	Jeffrey J. Norris	2316.1485US01	3383

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MERCHANT & GOULD PC  
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MINNEAPOLIS, MN 55402-0903

EXAMINER
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LEON, EDWIN A

ART UNIT	PAPER NUMBER
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2833

DATE MAILED: 05/02/2003

Please find below and/or attached an Office communication concerning this application or proceeding.

<b>Office Action Summary</b>	Application No.	Applicant(s)
	09/939,199	NORRIS ET AL.
	Examiner Edwin A. León	Art Unit 2833

-- The MAILING DATE of this communication appears on the cover sheet with the corresponding address --  
**Period for Reply**

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133).
- Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

#### Status

- 1) Responsive to communication(s) filed on 08 April 2003.
- 2a) This action is FINAL.                    2b) This action is non-final.
- 3) Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

#### Disposition of Claims

- 4) Claim(s) 1, and 3-15 is/are pending in the application.
  - 4a) Of the above claim(s) \_\_\_\_\_ is/are withdrawn from consideration.
- 5) Claim(s) 15 is/are allowed.
- 6) Claim(s) 1,3-8 and 10-14 is/are rejected.
- 7) Claim(s) 9 is/are objected to.
- 8) Claim(s) \_\_\_\_\_ are subject to restriction and/or election requirement.

#### Application Papers

- 9) The specification is objected to by the Examiner.
- 10) The drawing(s) filed on \_\_\_\_\_ is/are: a) accepted or b) objected to by the Examiner.  
 Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
- 11) The proposed drawing correction filed on \_\_\_\_\_ is: a) approved b) disapproved by the Examiner.  
 If approved, corrected drawings are required in reply to this Office action.
- 12) The oath or declaration is objected to by the Examiner.

#### Priority under 35 U.S.C. §§ 119 and 120

- 13) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
  - a) All
  - b) Some \*
  - c) None of:
    1. Certified copies of the priority documents have been received.
    2. Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.
    3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).
- \* See the attached detailed Office action for a list of the certified copies not received.
- 14) Acknowledgment is made of a claim for domestic priority under 35 U.S.C. § 119(e) (to a provisional application).
  - a) The translation of the foreign language provisional application has been received.
- 15) Acknowledgment is made of a claim for domestic priority under 35 U.S.C. §§ 120 and/or 121.

#### Attachment(s)

- |   |  |
|---|--|
| 1) <input type="checkbox"/> Notice of References Cited (PTO-892)  | 4) <input type="checkbox"/> Interview Summary (PTO-413) Paper No(s). _____ . |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948)                          | 5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152)  |
| 3) <input checked="" type="checkbox"/> Information Disclosure Statement(s) (PTO-1449) Paper No(s) <u>13</u> . | 6) <input type="checkbox"/> Other: _____                                     |

## DETAILED ACTION

### ***Response to Amendment***

1. Applicant's Request for Continued Examination and amendment filed April 8, 2003 in which Claims 1, 4, 6, 8-10 and 13 have been amended and new Claim 15 has been added, have been placed of record in the file as Papers No. 11 and 12.

### ***Claim Rejections - 35 USC § 102***

2. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

3. Claims 1, 3-8 and 10-14 are rejected under 35 U.S.C. 102(e) as being anticipated by Burroughs et al. (U.S. Patent No. 4,840,568). With regard to Claim 1, Burroughs et al. discloses a system for use with jack assemblies (12) including front plug receiving ports (104,105,106) and rear electrical card edge contacts (601-612) comprising: a chassis (320) having a front and a rear, the chassis (320) including a power bus (See Column 10, Lines 34-67 and Column 11, Lines 1-26) having a plurality of power plugs (See Column 10, Lines 34-67 and Column 11, Lines 1-26) for providing electrical power, the power bus (See Column 10, Lines 34-67 and Column 11, Lines 1-

26) further including a power intake (See Column 10, Lines 34-67 and Column 11, Lines 1-26) for receiving electrical power, the chassis (320) defining a plurality of jack assembly receiving areas (where 10 and 12 are mounted) formed in the front of the chassis (320); and a plurality of mount apparatus (10) mounted in the chassis (320), each of the mount apparatus (10) including a power receptacle (302) for receiving electrical power from one of the plurality of power plugs (See Column 10, Lines 34-67 and Column 11, Lines 1-26) and a circuit board assembly (18), each of the mount apparatus (10) including front electrical contacts (300) and rear electrical contacts (302), the front electrical contacts (300) configured for contacting the rear electrical card edge contacts (601-612) of the jack assemblies (12). See Figs. 1-16, Column 10, Lines 34-67 and Column 11, Lines 1-26.

With regard to Claim 3, Burroughs et al. discloses the chassis (320) further including first and second cable guides (See Column 10, Lines 34-67 and Column 11, Lines 1-26). See Figs. 1-16, Column 10, Lines 34-67 and Column 11, Lines 1-26.

With regard to Claim 4, Burroughs et al. discloses a plurality of jack assemblies (12) each having front and plug receiving ports (104,105,106) and rear electrical card edge contacts (601-612) that are electrically connected to the mount apparatus (10). See Figs. 1-16, Column 10, Lines 34-67 and Column 11, Lines 1-26.

With regard to Claim 5, Burroughs et al. discloses the chassis (320) including slots (See Column 10, Lines 34-67 and Column 11, Lines 1-26) for retaining the jack assembly (12). See Figs. 1-16, Column 10, Lines 34-67 and Column 11, Lines 1-26.

With regard to Claim 6, Burroughs et al. discloses the mount apparatus (10) including: a front cover (200) having a plurality of receptacles (302); a back cover (Fig. 11) having a plurality of through holes (208); and wherein the circuit board assembly (18) is sandwiched between the front cover (200) and the back cover (Fig. 11), the rear electrical contacts (302) of the mount apparatus (10) including a plurality of pins (601-612) extending through the holes (208) in the back cover (Fig. 11). See Figs. 1-16, Column 10, Lines 34-67 and Column 11, Lines 1-26.

With regard to Claim 7, Burroughs et al. discloses the circuit board assembly (18) including a board (18) having a plurality of through holes (18a) aligned with the receptacles (302) of the front cover (200) and the through holes (208) of the back cover (Fig. 11); a plurality of contacts (601-612) retained in a first set of the through holes (18a) of the board (18) of the circuit board assembly (18), a first end of each contact (601-612) defining each of the front electrical contacts (300) and being extended towards and exposed in a corresponding receptacle (302) of the front cover (200) and stopped by the front cover (200), a second end of each contact (601-612) being extended towards and projecting toward the back cover (Fig. 11); the plurality of pins (601-612) retained in a second set of the through holes (18a) of the board (18) of the circuit board assembly (18), a first end of each pin (Column 12, Lines 34-49) being extended towards and stopped by the front cover (200), a second end of each pin (601-612) being extended towards and projected from a corresponding through hole (208) of the back cover (Fig. 11); and a trace (500) electrically connecting each contact (601-

612) to each corresponding pin (601-612). See Figs. 1-16, Column 10, Lines 34-67 and Column 11, Lines 1-26.

With regard to Claim 8, Burroughs et al. discloses the circuit board assembly (18) includes a circuit board (18) and a plurality of electrical terminals (300), the electrical terminals (300) including the front electrical contacts (300) of the mount apparatus (10). See Figs. 1-16, Column 10, Lines 34-67 and Column 11, Lines 1-26.

With regard to Claim 10, Burroughs et al. discloses a system for use with jack assemblies (12) including front plug receiving ports (104,105,106) and rear electrical card edge contacts (601-612) comprising: a chassis (320) defining a plurality of slots configured to receive top and bottom edges of the jack assemblies (12), a plurality of mount apparatus (10) mounted in the chassis (320), each of the mount apparatus (10) including: a front cover (200) having a plurality of receptacles (302) including electrical contacts for mating with the rear card edge electrical contacts (601-612) of the jack assemblies (12); a back cover (Fig. 11) having a plurality of through holes (208); and a circuit board assembly (18) sandwiched between the front cover (200) and the back cover (Fig. 11), the circuit board assembly (18) including a plurality of pins (601-612) extending through the holes (208) of the back cover (Fig. 11), the circuit board assembly (18) providing electrical communication between the electrical contacts of the front cover (200) and the pins (601-612) extending through the back cover (Fig. 11).

See Figs. 1-16, Column 10, Lines 34-67 and Column 11, Lines 1-26.

With regard to Claim 11, Burroughs et al. discloses the circuit board assembly (18) including: a board (18) having a plurality of through holes (208) aligned with the

receptacles (302) of the front cover (200) and the through holes (208) of the back cover (Fig. 11); a plurality of contacts (601-612) retained in a first set of the through holes (208) of the board (18) of the circuit board assembly (18), a first end of each contact (601-612) defining each of the electrical contacts of the front cover (200) and being extended towards and exposed in a corresponding receptacle (302) of the front cover (200) and stopped by the front cover (200), a second end of each contact (601-612) being extended towards and projecting toward the back cover (Fig. 11); a plurality of pins (601-612) retained in a second set of the through holes (208) of the board (18) of the circuit board assembly (18), a first end of each pin (601-612) being extended towards and stopped by the front cover (200), a second end of each pin (601-612) being extended towards and projected from a corresponding through hole (208) of the back cover (Fig. 11); and a trace (500) electrically connecting each contact (601-612) to each corresponding pin (601-612). See Figs. 1-16, Column 10, Lines 34-67 and Column 11, Lines 1-26.

With regard to Claim 12, Burroughs et al. discloses the chassis (320) further including first and second cable guides (See Column 10, Lines 34-67 and Column 11, Lines 1-26). See Figs. 1-16, Column 10, Lines 34-67 and Column 11, Lines 1-26.

With regard to Claim 13, Burroughs et al. discloses a plurality of jack assemblies (12) each having electrical card edge contacts (601-612) that are electrically connected to the mount apparatus (10). See Figs. 1-16, Column 10, Lines 34-67 and Column 11, Lines 1-26.

With regard to Claim 14, Burroughs et al. discloses the chassis (320) including slots (See Column 10, Lines 34-67 and Column 11, Lines 1-26) for retaining the jack assembly (12). See Figs. 1-16, Column 10, Lines 34-67 and Column 11, Lines 1-26.

***Allowable Subject Matter***

4. Claim 15 is allowed for the reasons given on the Office Action of December 4, 2002.
5. Claim 9 is objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims for the reasons given on the Office Action of December 4, 2002.

***Response to Arguments***

6. Applicant's arguments with respect to claims 1 and 3-15 have been considered but are moot in view of the new ground(s) of rejection.

***Conclusion***

7. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Edwin A. León whose telephone number is (703) 308-6253. The examiner can normally be reached on Monday - Friday 9:00-5:30.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Paula A. Bradley can be reached on (703) 308-2319. The fax phone numbers for the organization where this application or proceeding is assigned are (703) 308-7722 for regular communications and (703) 308-7722 for After Final communications.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number is (703) 308-0956.

*Edwin A. León*

Edwin A. León  
AU 2833

EAL  
April 30, 2003

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